

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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In re Patent Application of

Kazutaka Shibata and Tsunemori Yamaguchi

Serial No. 09/204,123

Filed: December 2, 1998

For: METHOD OF SURFACE-
MOUNTING ELECTRONIC
COMPONENTS

Assistant Commissioner for Patents
Washington, D.C. 20231

Group Art Unit: 3729

Examiner: D. Caputo

San Francisco, California



CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231 on April 26, 2000.

Valerie Olsen

Valerie Olsen

4/26/00

Date

AMENDMENT "A"

Sir:

In response to the Office Action dated October 27, 1999 in the above-referenced application, regarding which it is requested with the payment of a fee to extend the period for response by three months to April 27, 2000, applicant submits the following REMARKS:

R E M A R K S

Claims 1, 2, 4, 6 and 7 were rejected under 35 U.S.C. 102 as being anticipated by Matsui, the Examiner particularly pointing to Fig. 5(a) of this reference. Rejection of a claim under 35 U.S.C. 102 is justified only when each of the inventive elements in that claim is disclosed in one reference. Matsui does not disclose every inventive element in independent claim 1, and hence it is believed that the Examiner's rejection of claim 1 and the claims dependent therefrom is not justified and hence should be reversed.